

Receipt date: 06/24/2008

Doc code :IDS

Doc description: Information Disclosure Statement (IDS) Filed

10539334 - GAU: 2825

PTO/SB/08a (03-08)

Approved for use through 06/30/2008. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10539334
	Filing Date		2005-06-15
	First Named Inventor	Geetha Arthanari, et al.	
	Art Unit	2825	
	Examiner Name	Suchin Parihar	
	Attorney Docket Number	BUR920020005US1	

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
/SP/	1	5790841		1998-08-04	Scherer, et al.	
/SP/	2	6266803		2001-07-24	Scherer, et al.	
/SP/	3	5917729		1999-06-29	Naganuma, et al.	
/SP/	4	5610833		1997-03-11	Chang, et al.	
/SP/	5	5774371		1998-06-30	Kawakami, et al.	
/SP/	6	6223329		2001-04-24	Ling, et al.	
/SP/	7	5787268		1998-07-28	Sugiyama, et al.	
/SP/	8	6513149		2003-01-28	Donato	
If you wish to add additional U.S. Patent citation information please click the Add button.						

Receipt date: 06/24/2008

Application Number

10539334

10539334 - GAU: 2825

Filing Date

2005-06-15

First Named Inventor

Geetha Arthanari, et al.

Art Unit

2825

Examiner Name

Suchin Parihar

Attorney Docket Number

BUR920020005US1

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Not for submission under 37 CFR 1.99)

U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/SP/	1	20010028265		2001-10-11	Nitta	
/SP/	2	20010025368		2001-09-27	Cooke, et al.	

If you wish to add additional U.S. Published Application citation information please click the Add button.

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
/SP/	1	JP8129571	JP		1996-05-21			<input type="checkbox"/>
/SP/	2	JP2001125037	JP		2001-05-11			<input type="checkbox"/>
/SP/	3	JP8044776	JP		1996-02-16			<input type="checkbox"/>
/SP/	4	Abstrt 571	JP		1996-05-21			<input checked="" type="checkbox"/>
/SP/	5	Abstrt 037	JP		2001-05-11			<input checked="" type="checkbox"/>
/SP/	6	Abstrt 776	JP		1996-02-16			<input type="checkbox"/>

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /SP/

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10539334	10539334 - GAU: 2825
	Filing Date		2005-06-15	
	First Named Inventor	Geetha Arthanari, et al.		
	Art Unit	2825		
	Examiner Name	Suchin Parihar		
	Attorney Docket Number	BUR920020005US1		

If you wish to add additional Foreign Patent Document citation information please click the Add button

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
/SP/	1	S. SUMINAGA - "Clock-Skew Compensation Mechanism for Large Scale Integration - IBM Technical Disclosure Bulletin - Vol.. 38, No. 8, August 1995 - pp. 141-142	<input type="checkbox"/>
/SP/	2	KEITH M. CARRIG, et al - "A Clock Methodology For High-Performance Microprocessors - Journal of VLSI Signal Processing (1997) - pp. 105-111	<input type="checkbox"/>
/SP/	3	ANN MARIE RINCON, et al. - "A Proven Methodology for Designing One-Million-Gate ASICs" - IEEE 1996 Custom Integrated Circuits Conference - pp. 45-52	<input type="checkbox"/>

If you wish to add additional non-patent literature document citation information please click the Add button

EXAMINER SIGNATURE

Examiner Signature	/Suchin Parihar/	Date Considered	06/27/2008
--------------------	------------------	-----------------	------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.